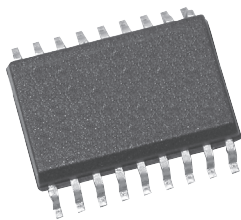


CA TRANSLATOR V1



Actual Size

Color Kinetics® CA Translator V1 is a microchip designed to convert standard data inputs to Chromasic® data—Color Kinetics' proprietary data protocol. CA Translator V1 allows for easy development of Chromasic-based products with industry standard control interfaces which reduces system integration efforts and overall time to market.

CA Translator V1 supports DMX512 and RS-232 data inputs. The input formats and parameters, such as baud rate, are user selectable at set-up. A CA Translator V1 Developers' Kit is available to simplify design and development with Chromasic based products. The Developers' Kit includes a translator chip mounted onto a printed circuit board with common power and data connectors as well as dip-switches for input parameter configuration. This kit provides a useful development platform out of the box.

CA TRANSLATOR V1 SPECIFICATIONS

OPERATING VOLTAGE	2.0V to 5.5V
DATA RATE RANGE	9Kbps to 600Kbps
PACKAGES	18-pin SOIC
CAPABILITIES	Supports 140 Chromasic light node

CHROMASIC® BY COLOR KINETICS

CA Translator V1 ITEM# 118-000055-00

This product is protected by one or more of the following patents: U.S. Patent Nos. 6,016,038, 6,150,774 and other patents listed at <http://colorkinetics.com/patents/>. Other patents pending.

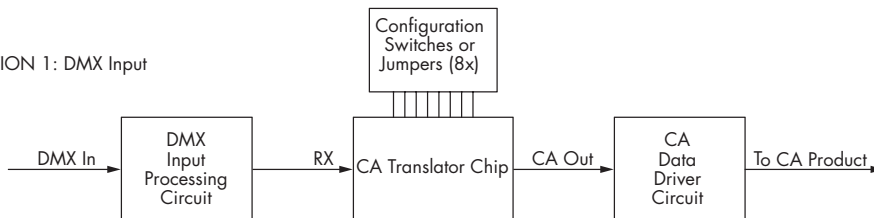
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BRO160 Rev 01

Specifications subject to change without notice.

OPTION 1: DMX Input



OPTION 2: RS-232 Input Jumper or Hardware

